

Program of the 2nd Workshop on Mapping of Applications to MPSoCs

Monday, June 29th, 2009

11:45	Lunch	Finger food
12:20	Peter Marwedel (TU Dortmund, D)	Opening
12:30	Soonhoi Ha (Seoul National U., KR)	Programming-platform based Design of MPSoCs: The HOPES Approach
13:15	Jürgen Teich (U. Erlangen, D)	SystemCoDesigner - Automatic Design Space Exploration and Prototyping from Behavioral Models
14:00	Qiang Xu (Chinese University, Hong Kong, CN)	Minimizing Energy Consumption of MPSoCs under Lifetime Reliability Constraints
14:45	Tajana Simunic (UCSD, US)	Energy and thermal management in MPSoCs
15:30	Break	
16:00	E. Deprettere (U Leiden, NL) with contributions by Todor Stefanov (U. Leiden, NL), Iuliana Bacivarov (ETHZ, CH) and Bastian Ristau (TU Dresden, D)	Benchmarking
16:55	Björn Franke (U. Edinburgh, UK)	Towards a Holistic Approach to Auto-Parallelization: Integrating Profile-Driven Parallelism Detection and Machine-Learning Based Mapping
17:20	Marco Bekooij (NXP, NL)	Automatic parallelization for embedded multiprocessor systems with non-uniform memory access latencies
17:45	Fabrizio Ferrandi (Polytecnico di Milano, I)	Mapping and Scheduling of Parallel C Applications with Ant Colony Optimization onto Heterogeneous Reconfigurable MPSoCs
18:10	Session ends	
18:30	Departure for social event	
19:00	Social event: cruise on the Rhine river (passing the Loreley rock)	

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9:00	Per Larsen (TU Lyngby, DK)	Data-dependencies and Thread Interaction in Parallel Loops
9:25	Michael Claßen (U. Passau, D)	Introducing the GCC to the Polyhedron Model
9:50	Gerard Smit (TU Twente, NL)	Run-time Spatial Mapping on MPSoCs
10:15	Break	
10:45	Iuliana Bacivarov (ETHZ, CH)	An Integrated Software Design Flow for Mapping Stream-Oriented Applications to Tile-Based MPSoCs
11:10	Anastasia Stulova (RWTH Aachen, D)	MAPS and a high-level virtual platform (MVP)
11:35	Michele Lombardi (U. Bologna, I)	Throughput constraint for Synchronous Data Flow Graphs
12:00	Lunch	
13:15	Henrik Theiling (AbsInt, D)	How Unthoughtful Resource Sharing Counteracts WCET-Analysis
13:40	Micrean Negrean (TU Braunschweig, D)	Timing Analysis on Complex Real-Time Automotive Multicore Architectures
14:05	Discussion	
14:45	Tea break/end of workshop	
15:15	Mneme project demo	See www.mneme.org ; Open to everyone, not part of the ArtistDesign workshop.
18:00	Close	

All presentation slots include the time for discussions.

More information about the workshop: <http://www.artist-embedded.org/artist/-map2mpsoc-2009-.html>

Workshop fee: 100 €

Registration:

- for the workshop: by e-mail to Silke.Sandmann@tu-dortmund.de
- for accomodation: by fax to the hotel (see web site)

Deadlines for registration: May 31st.

This workshop is supported by the 7th Framework Program of European Commission via the ArtistDesign network of excellence (see www.artist-embedded.org) and held in cooperation with the hipeac network of excellence.